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IN THE UNITED STATES DESIGNATED OFFICE (DO/EO/US) **IAP20 RESUBMITTED 30 JAN 2006**

ATTY DKT. # ONS00396

Date : 1/24/2006
IN RE APPLICATION OF : Francois LHermite
APPLN. NO. : PCT/US2003/030581
FILED : 9/23/2003
FOR :
CIRCUIT : NEGATIVE CURRENT COMPENSATION METHOD AND

DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SIR:

It is respectfully requested that the prior art listed on FORM PTO-1449 be considered in the examination of the subject application and made of record therein. A copy of the listed prior art is enclosed herewith.

No representation is made or intended that the listed prior art enclosed herewith is material to patentability of the subject patent application.

No representation is made or intended that a search has been made or that no better prior art than that listed is available.

Respectfully submitted,
Francois LHermite



Kevin B. Jackson

ON Semiconductor
Intellectual Property Dept.
P.O. Box 62890 - A700
Phoenix, AZ 85082-2890

Reg. No. 38,502
Phone: 602-244-4885
Fax: 602-244-3619

Customer #: 27255

Sheet 1 of 1

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	ATTY. DOCKET NO. ONS00396		SERIAL NO. PCT/US2003/030581 10/566565
	APPLICANT Francois LHermite		
	FILING DATE 23 September 2003	GROUP	

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5784231	7/21/1998	Majid et al.	H02H	7/00	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date Pertinent Pages, Etc.)

	Data Sheet, Integrated Circuits "TEA1533P; TEA1533AP GreenChip TM II SMPS control IC", August 23 2002, Philips Semiconductors, 23 pps.
	Data Sheet, Integrated Circuits "TEA1552 GreenChip TM II SMPS control IC", August 27 2002, Philips Semiconductors, 23 pps.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.